

Patent Abstracts of Japan

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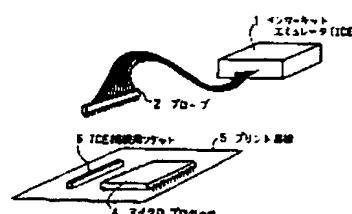
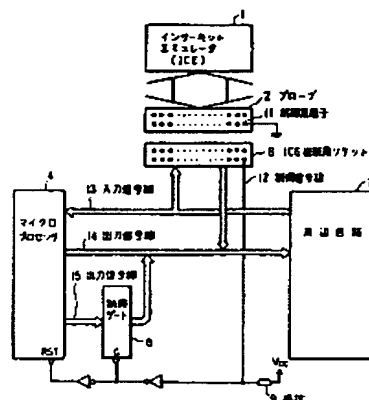
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TITLE : CONNECTION SYSTEM FOR  
IN-CIRCUIT EMULATOR



ABSTRACT : PURPOSE: To test a printed board with use of an in-circuit eliminator (ICE) without removing a processor cut of the printed board, by breaking the connection between a microprocessor and a peripheral circuit and at the same time connecting a signal line led from the ICE to the peripheral circuit via a socket for ICE.

CONSTITUTION: A microprocessor 4 is mounted on a printed board and a probe 2 of an ICE1 is connected to an ICE connecting socket 6 set on the printed board. An input signal line 13 of the processor 4 of the socket 6 is connected to a peripheral circuit 7 together with an output signal line 14 of the processor 4. while an output signal line 15 led from the processor 4 is connected to the socket 6 and the circuit 7 via a control gate 8. Furthermore a reset terminal RST of the processor 4, a control terminal G of the gate 8 and a power supply Vcc are connected to a control signal line 12 of the socket 6. Then the signal line led from the ICE1 is connected to the circuit 7 via the socket 6. Thus it is possible to test the processor 4 via the ICE1 without removing the processor out of the printed board.

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